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Comments:

Attorney Docket No.: 333772000900

Group Art Unit: 2863 Examiner: S. Kundu

Application No. 10/772,327 Filed: February 6, 2004

Inventor(s): Ankan PRAMANICK et al.

Title: METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS

Papers Attached:

- 1. Transmittal Form (1 page)
- 2. Amendment After Final Action (8 pages)

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U.S. Petent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Application Number 10/772.327 Filing Date TRANSMITTAL February 6, 2004 **FORM** First Named Inventor Ankan PRAMANICK 2863 (to be used for all correspondence after initial filing) Examiner Name S. Kundu Allomey Docket Number 333772000900 9 Total Number of Pages In This Submission **ENCLOSURES** (Check all that apply) After Allowance Communication Fee Transmittal Form Drawing(s) Appeal Communication to Board of Fee Attached Licensing-related Papers Appeals and Interferences Appeal Communication to TC x | Amendment/Reply Petition (Appeal Notice, Brief, Roply Brief) Petition to Convert to a Proprietary Information x After Final (8 pages) Provisional Application Power of Attorney, Revocation Affidavits/declaration(s) Status Letter Change of Correspondence Address X Other Enclosure(s) (please Identify below): **Extension of Time Request** Terminal Disclaimer Facsimile Cover Sheet **Express Abandonment Request** Request for Refund Information Disclosure Statement CD, Number of CD(s) **Certified Copy of Priority** Landscape Table on CD Document(s) Reply to Missing Parts/ Remarks Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT Firm Name MORRISON & FORRSTER LLP (Customer No. 25226) Signature Printed name Thomas Chan Date Reg. No. 51,543 December 5, 2005

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Docket No.: 333772000900

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ankan PRAMANICK et al.

Application No.: 10/772,327

Confirmation No.: 4514

Filed: February 6, 2004

Art Unit: 2863

For: METHOD AND APPARATUS FOR TESTING

INTEGRATED CIRCUITS

Examiner: S. Kundu

AMENDMENT AFTER FINAL ACTION (37 C.F.R. SECTION 1.116)

MS AF
EXPEDITED PROCEDURE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

This is in response to the final Office Action dated October 3, 2005 (Paper No. 06022005), for which a response is due on January 3, 2006. This response is filed within two months of the issuance of the final Office Action and therefore qualifies for expedited review. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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